IN THE CLAIMS:

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~strikethrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

Please CANCEL claims 3-14, 16-24, 26 and 27 and ADD new claims 28-37 in accordance with the following:

1. (CANCELLED)

2. (CANCELLED)

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27. (CANCELLED)

28. (NEW) A method for driving a plasma display panel device having first and second electrodes spaced apart from one another, and performing a display by applying voltage pulses to the first and second electrodes, comprising, during a sustain period:

applying a first pulse with positive polarity to the first electrodes while applying a second pulse with negative polarity to the second electrodes; and

applying a third pulse with negative polarity to the first electrodes while applying a fourth pulse with positive polarity to the second electrodes;

wherein the third pulse is applied to the first electrodes without connecting the first electrodes to ground potential, after the first pulse being applied thereto, and the fourth pulse is applied to said second electrodes without connecting the second electrodes to the ground potential, after the second pulse being applied thereto.

29. (NEW) The method for driving plasma display panel device according to claim 28, wherein:

the first and fourth pulses have a common potential and the second and third pulses have a common potential.

30. (NEW) The method for driving a plasma display panel device according to claim 28, wherein:

the third pulse is applied to the first electrodes after connecting the first electrodes to a first potential, different from the ground potential, after the first pulse being applied thereto, and the fourth pulse is applied to the second electrodes after connecting the second electrodes to the first potential, after the second pulse being applied thereto.

31. (NEW) The method for driving a plasma display panel device according to claim 30, wherein:

the first potential is positive polarity potential.

32. (NEW) The method for driving plasma display panel device according to claim 30, wherein:

the first potential is negative polarity potential.

33. (NEW) The method for driving plasma display panel device according to claim 28, wherein the plasma display panel further has third electrodes crossing the first and second electrodes, the method further comprising:

connecting the third electrodes to the ground potential during the sustain period.

34. (NEW) A plasma display panel having first electrodes, second electrodes and cells and producing displays at the cells by applying voltages to the first and second electrodes, comprising:

a positive power supply;

a negative power supply;

first and second transistors connected to the first electrode; and

third and fourth transistors connected to the second electrode;

wherein the first and the fourth transistors are connected to the positive power supply and the second and third transistors are connected to the negative power supply, and

when applying a positive pulse to the first electrode, current flows from the positive power supply, the first transistor, the first electrode, the cells, the second electrode and the third transistor to the negative power supply.

35. (NEW) The plasma display panel according to claim 34, further comprising:

fifth and sixth transistors connected to the first electrode;

seventh and eighth transistors connected to the second electrode; and

the fifth, sixth, seventh and eighth transistors being connected to a first potential which is different from ground potential and has a potential level between the potential of the positive power supply and the potential of the negative power supply.

36. (NEW) The plasma display panel according to claim 34, wherein during a sustain period:

the first transistor becomes ON to apply a first pulse with positive polarity to the first electrode while the fourth transistor becomes ON to apply a second pulse with negative polarity to the second electrode;

the second transistor becomes ON to apply a third pulse with negative polarity to the first electrodes while the third transistor becomes ON to apply a fourth pulse with positive polarity to the second electrode; and

the third pulse is applied to the first electrode without connecting the first electrode to ground potential, after the first pulse being applied thereto, and the fourth pulse is applied to the second electrode without connecting the second electrode to the ground potential, after the second pulse being applied thereto.

37. (NEW) A plasma display panel according to claim 35, further comprising:

third electrodes crossing the first and second electrodes and connected to the ground potential during the sustain period.